

	Document ID	Issue Date	Pages	Title	Current OR
1	US 20040136444 A1	20040715	29	Cascade filter receiver for DS-CDMA communication systems	375/147
2	US 20030215004 A1	20031120	12	Method for multiple access interference suppression, and receiver	375/148
3	US 20030198283 A1	20031023	14	Dynamic pilot filter bandwidth estimation	375/147
4	US 20030026326 A1	20030206	14	Method and apparatus for adjusting delay in systems with time-burst pilot and fractionally spaced equalizers	375/148
5	US 20020085624 A1	20020704	37	Multipath processor	375/148
6	US 20020080861 A1	20020627	14	Methods and apparatus to despread dual codes for CDMA systems	375/147
7	US 20020080860 A1	20020627	14	Methods and apparatus to despread dual codes for CDMA systems	375/147
8	US 20010012316 A1	20010809	19	Rake receiver with low pass filer	375/148
9	US 6760362 B2	20040706	14	Dynamic pilot filter bandwidth estimation	375/147
10	US 6690715 B2	20040210	14	Rake receiver with embedded decision feedback equalizer	375/148
11	US 6636561 B1	20031021	11	Channel equalisers	375/232
12	US 6611548 B2	20030826	39	Multipath processor	375/148
13	US 6330272 B1	20011211	38	Receiving a spread spectrum data signal using determined weights	375/147
14	US 6327298 B1	20011204	9	Post-correlation temporal nulling	375/148
15	US 6320896 B1	20011120	7	RF receiver having frequency-hopping/direct-sequence spread spectrum signal discrimination	375/130
16	US 6181731 B1	20010130	19	Spread spectrum communication device	375/144

	<b>Current XRef</b>	<b>Inventor</b>
<b>1</b>	375/350	Massicotte, Daniel et al.
<b>2</b>		Vihriala, Jaakko
<b>3</b>		Patel, Shimman et al.
<b>4</b>	375/234	Jayaraman, Srikant et al.
<b>5</b>		Lomp, Gary R.
<b>6</b>		Wei-Ku, Chung et al.
<b>7</b>		Wei-Ku, Chung et al.
<b>8</b>	375/149	Maruyama, Yuichi
<b>9</b>	375/350	Patel; Shimman et al.
<b>10</b>	375/150; 375/152	Webster; Mark A. et al.
<b>11</b>	375/148; 375/235; 375/260; 712/19	Hudson; John E
<b>12</b>	375/130; 375/331; 375/370	Lomp; Gary R.
<b>13</b>		Lomp; Gary R.
<b>14</b>	342/378; 342/383; 375/150	Grobert; Paul H.
<b>15</b>	375/136; 375/147	Jovanovich; Alan F. et al.
<b>16</b>	375/148; 375/346; 375/350	Kochi; Tetsuya et al.

	Document ID	Issue Date	Pages	Title	Current OR
17	US 6111911 A	20000829	52	Direct sequence frequency ambiguity resolving receiver	375/147
18	US 5995538 A	19991130	40	Spread spectrum multipath processor system and method	375/142
19	US 5974101 A	19991026	76	Spread spectrum modulation communication apparatus for narrow band interference elimination	375/350
20	US 5673286 A	19970930	42	Spread spectrum multipath processor system and method	375/148

	<b>Current XRef</b>	<b>Inventor</b>
<b>17</b>	375/141	Sanderford, Jr.; H. Britton et al.
<b>18</b>	375/143; 375/146	Lomp; Gary R.
<b>19</b>	375/148; 375/349; 455/307	Nago; Hidetada
<b>20</b>	375/150; 375/152	Lomp; Gary R.